



A Unit of A. Shama Rao Foundation  
**Srinivas Institute of Technology, Merlapadavu, Mangaluru - 574 143**

Approved by AICTE New Delhi, Govt. of Karnataka, Bengaluru

Affiliated to Visvesvaraya Technological University, Belagavi

Phone No.: (0824)-2425966, 2421566, 2444891 Fax: (0824)-2442766, 2423302 Website:

www.srinivasgroup.com

**3.2.2 Number of workshops/seminars/conferences including programs conducted on Research Methodology, Intellectual Property Rights (IPR) and entrepreneurship during the last five years.**

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## Srinivas Institute of Technology`

*(Approved by AICTE New Delhi, Govt. of Karnataka,  
Bengaluru Affiliated to Visvesvaraya Technological  
University, Belagavi) Valachil, Merlapadavu,  
Mangaluru - 574 143*

### **Preamble**

In the pursuit of academic excellence and holistic development, Srinivas Institute of Technology (SIT) remains steadfast in its commitment to fostering an environment that nurtures innovation, research, and entrepreneurial spirit. The institution acknowledges the vital role that workshops, seminars, conferences, and specialized programs play in shaping the intellectual landscape of our academic community. Over the past five years, SIT has consistently strived to enhance the academic experience by organizing a spectrum of events dedicated to Research Methodology, Intellectual Property Rights (IPR), and Entrepreneurship. These initiatives serve as pillars that fortify our commitment to providing students with a comprehensive and forward-looking education. As we reflect on our journey, it becomes evident that these workshops and programs have not only expanded the horizons of knowledge but have also contributed significantly to the overall growth of the SIT community. The exchange of ideas, the exploration of innovative methodologies, and the in-depth understanding of intellectual property and entrepreneurship have become integral components of our academic ethos.



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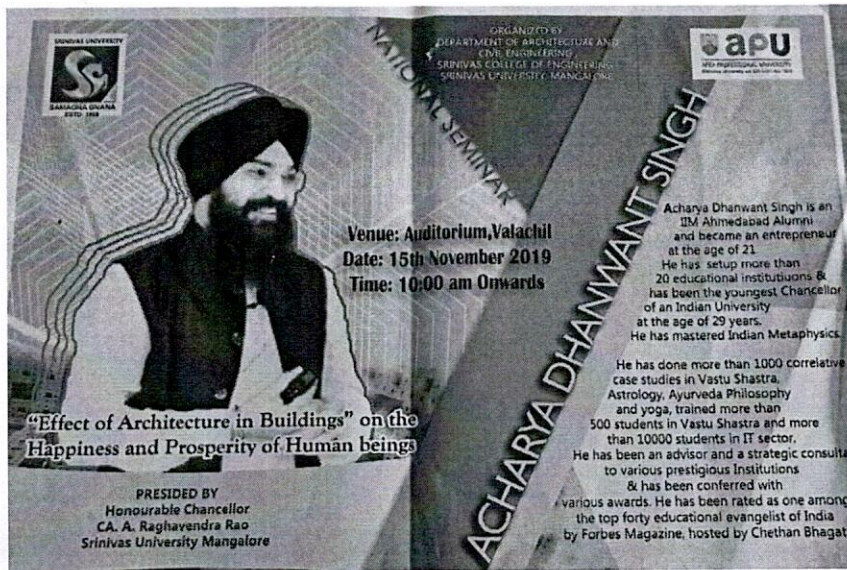
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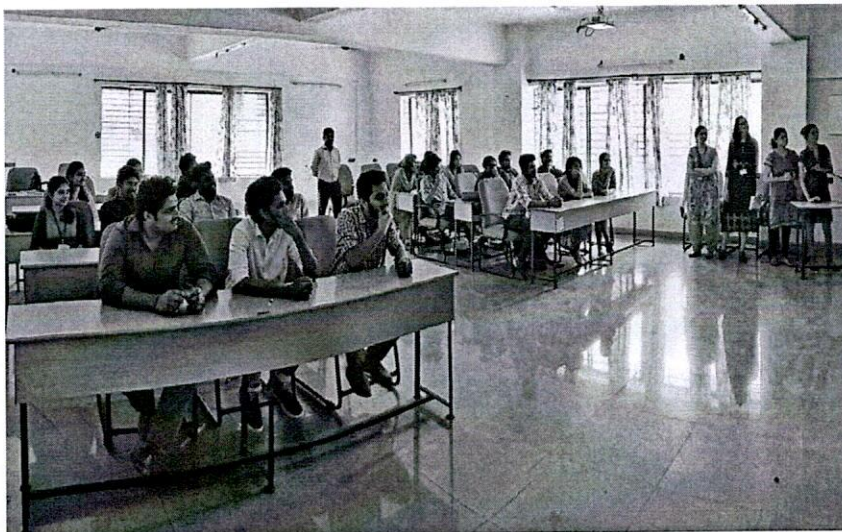
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**Seminar on "Effects of Architecture in Buildings-IKS"**



Poster of "Effects of Architecture in Buildings session"



Students attending Seminar on "Effects of Architecture in Buildings"

Venue: Architecture block SIT, Lat 12.87528<sup>0</sup> Long 74.939106<sup>0</sup>

  
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SRINIVAS INSTITUTE OF TECHNOLOGY  
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## Entrepreneurship Awareness Program

**SRINIVAS INSTITUTE OF TECHNOLOGY**  
**SIT INNOVATION COUNCIL**  
Accredited by NAAC  
VALACHIL, MANGALURU - 574 143



**SIT INNOVATION COUNCIL**  
Cordially invites you to the

**Workshop on " Entrepreneurship Awareness Program "**  
On 5<sup>th</sup> May, 2019

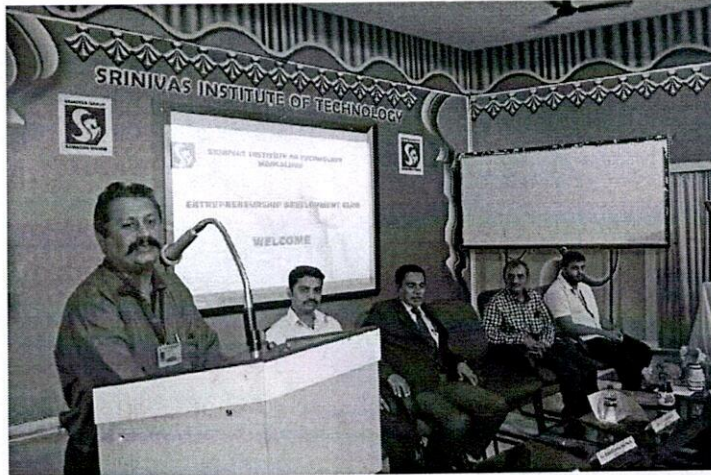
**Chief Guest**  
**Dr. Chidanand C.Gavimath**  
Special officer ED Cell, Visvesvarayya Technological University, Belagavi

**Presided by**  
**Dr. Shrinivasa Mayya D**  
Principal, Srinivas Institute of Technology

<b>Venue: Seminar hall SIT</b>	<b>Time: 10:00 AM</b>
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Extending a Hearty Welcome  
**Dr. Radhakrishnan, EDC Co-ordinator**  
**Secretary Mr. Sudarshan K Dept of CSE and**  
**Joint Secretary Ms. Sowmya, Dept of ISE.**

### *Invitation for Entrepreneurship Awareness program*



Inaugural function of Entrepreneurship Awareness Program

Venue : Seminar hall SIT , Lat 12.87528<sup>o</sup> Long 74.939106<sup>o</sup>

  
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**A Talk on Empowering Higher Education for Innovation and growth**

**SRINIVAS INSTITUTE OF TECHNOLOGY**  
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(Accredited by NAAC)

**ELITE**  
( ELECTRICAL INSIGHT IN TECHNICAL ENDEAVOUR )

**ORGANIZES**  
**THE TECHNICAL TALK**  
**ON**  
**A TALK ON EMPOWERING HIGHER EDUCATION FOR**  
**INNOVATION AND GROWTH**

Resource Person  
**MR. PRAKASH CHAKRAVARTHY**


April 2nd, 2019  
10 AM  
Research hall

Dr. Shrinivasa Mayya D  
Principal

Prof. Lokesh B  
HOD, EEE

Prof. Ganapathi Sharma K  
Coordinator, ELITE

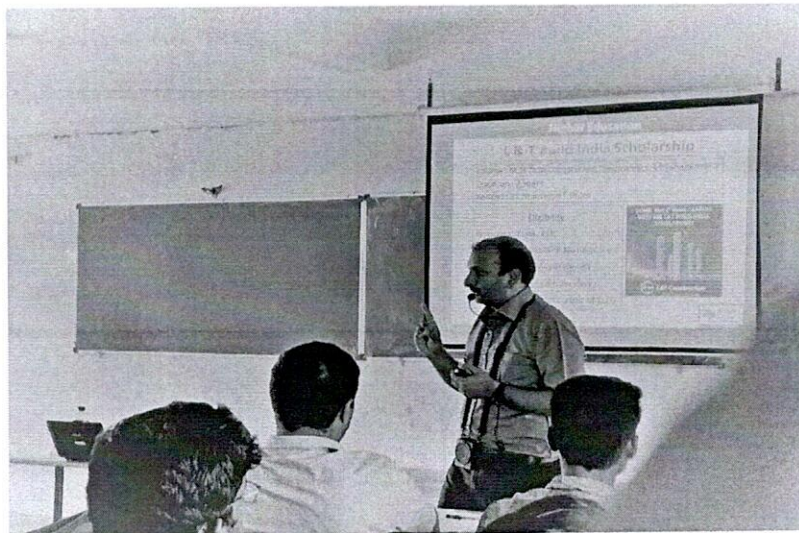
Staff and Students

  
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Technical Talk on opportunities in higher education by Mr. Prakash Chakravarthy

Venue: Research hall , Lat 12.87528° Long 74.939106°



Mr. Prakash Chakravarthy, briefly explaining about the opportunities in higher education

Venue: M501, Lat 12.87528°, Long 74.939106°



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## Workshop on Integrating ICT for Enhanced teaching learning & Research Invention

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Under Share And Mentor Institutions (Mangadarshan)  
Scheme Of AICTE

**"WORKSHOP ON INTEGRATING ICT  
FOR ENHANCED  
TRACKING LEARNING AND RESEARCH INNOVATION"**

By  
**RVCE Bengaluru and IQAC**

**18 FEB 2019**  
**10 AM**  
**MCA Lab**

**Dr. Shrinivasa Mayya, D**  
Principal

**Dr. B V Uma**  
professor RVCE and Chief  
Coordinator mangadarshan

**Dr. Thomas Pinto**  
IQAC chief Coordinator

**Staff and Students**

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**Department of M.C.A.**

**Seminar On Intellectual Property Rights**

**SRINIVAS INSTITUTE OF TECHNOLOGY**  
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(Accredited by NAAC)

**MASM (MCA Association of SIT Mangalore)**  
In collaboration with IPR cell

ORGANIZES  
TALK  
ON

**INTELLECTUAL  
PROPERTY RIGHTS**

**RESOURCE PERSON**  
**DR. AJAY S JOSEPH**  
HOD, MBA  
SIT, MANGALURU

27th November, 2018  
10 AM  
seminar hall

Dr. Shrinivasa Mayya D  
Principal

Dr. Shashidhar. Kini  
HOD, MCA

Prof. Girish Kunder  
Coordinator, IPR cell

Staff and Students

*Invitation for technical talk on Intellectual property rights*

  
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### A Talk on Intellectual Property Rights

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
ENGINEERING**

**ORGANIZES  
A TALK ON**

**INTELLECTUAL  
PROPERTY RIGHTS**

**RESOURCE PERSON  
MR.GOURISH S HEGDE  
ECE DEPARTMENT ,SIT**

**21st November, 2018**

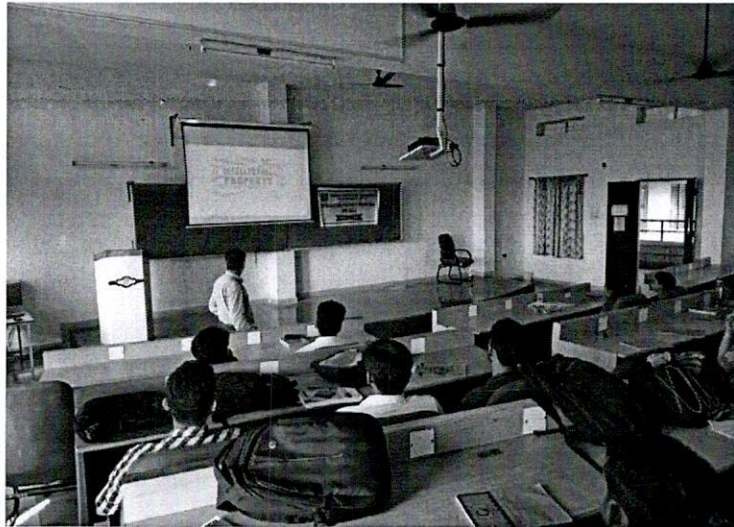
**Lecture hall**

**10 AM**

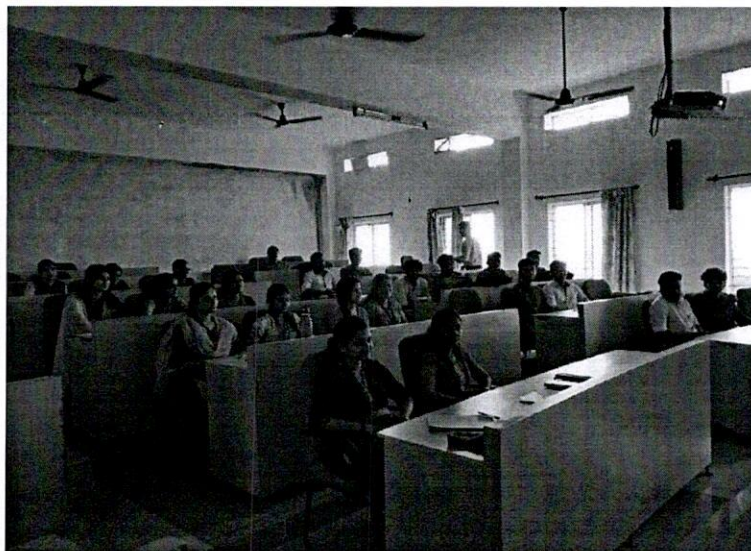
**Dr. Shrinivasa Mayya D**    **Prof. Dr. Soorya Krishna,**    **Prof. Chinmayaraj**  
**Principal**                      **HOD, ECE**                      **Kukket, Branch Coordinator,**  
**Staff and Students**

*Invitation for technical talk on Intellectual property rights*

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*Professor Gourish S Hegde delivering a talk on Intellectual Property Rights*



*Students attended talk on Intellectual Property rights*

Venue: Lecture hall 302, Lat 12.87528° Long 74.939106°



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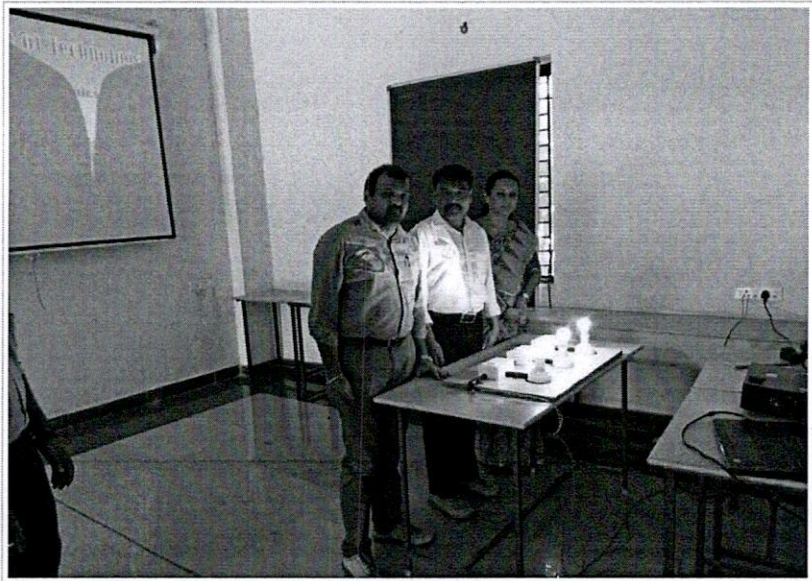
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**DEPARTMENT OF ELECTRICAL ENGINEERING**

**Empowering Innovation in Electrical Wiring Workshop**




Mr. Rakesh Sharma and Mrs. Deepthi Shetty reviewing the projects

Venue: Microcontroller Lab, Lat 12.87528° Long 74.939106°



Student and Coordinators with their workshop projects

Venue: Microcontroller Lab, Lat 12.87528° Long 74.939106°

  
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